IN THE CLAIMS:

Claims 1-6 and 11-13 are pending.

Claims 1-3 and 11-13 remain unchanged in this response.

Claims 5 and 6 are cancelled herein.

Claim 4 is amended herein.

The status of the claims is as follows:

1. (Previously amended) A memory interlace-checking method to detect weakened memory in a memory array composed of odd and even addresses, the method comprising:

sequentially performing accessing commands on the odd addresses in the memory array; and

sequentially performing data checking commands on the even addresses in the memory array that are complementary to the odd addresses.

2. (Previously amended) The method of claim 1, wherein the odd and even addresses are memory rows.

3. (Previously amended) The method of claim 1, wherein the odd and even addresses are memory columns.

4. (Currently amended) A memory interlace-checking method to detect weakened memory, the method comprising.

executing a test program ~~with command actions~~, wherein the testing program has:

at least a portion of main address accessing data, wherein the main address accessing data contains command actions; and

at least a portion of secondary address accessing data, which is at least partially complementary to the portion of main address accessing data, wherein the secondary address accessing data contains checking actions.

Claim 5 (Cancelled herein).

Claim 6 (Cancelled herein).

Claim 7 (Previously cancelled).

Claim 8 (Previously cancelled).

Claim 9 (Previously cancelled).

Claim 10 (Previously cancelled).

11. (Previously added) A memory interlace-checking method to detect weakened memory in a memory array composed of odd and even addresses, the method comprising:

sequentially performing accessing commands on the even addresses in the memory array; and

sequentially performing data checking commands on the odd addresses in the memory array that are complementary to the even addresses.

12. (Previously added) The method of claim 11, wherein the odd and even addresses are memory rows.

13. (Previously added) The method of claim 11, wherein the odd and even addresses are memory columns.